

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Application of

TIF-15767A

Gerard Chauvel, et al.

Examiner: D. Tran

Serial No.: (Rule 60 Continuation of 07/902,191)

Art Unit: 2302

Filed: herewith

For:

PROTOCOL PROCESSOR FOR THE EXECUTION OF A COLLECTION OF

INSTRUCTIONS IN A REDUCED NUMBER OF OPERATIONS

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents

Washington, D.C. 20231

Dear Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on July 10, 1997.

Ronald O. Neerings, Reg. No. 34 227

Please amend the above-identified application prior to examination of the above-identified application:

IN THE TITLE OF THE INVENTION:

Please change the title of the invention from "Protocol Processor Intended for the Execution of a Collection of Instructions in a Reduced Number of Operations" to --Multiple Processor Apparatus Having A Protocol Processor Intended for the Execution of a Collection of Instructions in a Reduced Number of Operations--.

IN THE ABSTRACT:

02 FC:103